

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kenichi Adachi

Confirmation No.: 5951

Application No.: 10/575,041

Group Art Unit: Not Yet Assigned

Filing Date: April 5, 2006

Examiner: Not Yet Assigned

International Application No.: PCT/JP2005/006965

Attorney Docket No.: 545/72

International Filing Date: April 8, 2005

For: Processor For Controlling Performance In
Accordance With A Chip Temperature,
Information Processing Apparatus, And Method
Of Controlling Processor

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**SUPPLEMENTAL PRELIMINARY AMENDMENT
PURSUANT TO 37 C.F.R. § 1.115**

Sir:

Prior to examining the above-identified patent application, please amend the application as follows: